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Subclass	
Class	
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED 882	PATENT DATE Q.A. A.G.
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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11028041 711 105 2016 M. K. Km

APPLICANTS
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2188

TITLE
Memory controller receiver circuitry with tri-state noise immunity

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS	CLAIMS ALLOWED	
	Sheets Drwg. Figs. Drwg. Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____	NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____	ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____	Amount Due	Date Paid
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